Sketch of Lecture 11

Example 71. Let us consider a baby version of CSS (introduced in the previous example). Our PRG uses the LFSR $x_{n+3} \equiv x_{n+1} + x_n \pmod{2}$ as well as the LFSR $x_{n+4} \equiv x_{n+2} + x_n \pmod{2}$. The output of the PRG is the output of these two LFSRs added with carry.

Adding with carry just means that we are adding bits modulo 2 but add an extra 1 to the next bits if the sum exceeded 1. This is the same as interpreting the output of each LFSR as the binary representation of a (huge) number, then adding these two numbers, and outputting the binary representation of the sum.

If we use (0,0,1) as the seed for LFSR-1, and (0,1,0,1) for LFSR-2, what are the first 10 bits output by our PRG?

Solution. With seed 0, 0, 1 LSFR-1 produces 0, 1, 1, 1, 0, 0, 1, 0, 1, 1, ...With seed 0, 1, 0, 1 LSFR-2 produces 0, 0, 0, 1, 0, 1, 0, 0, 0, 1, ...We now add these two:

	0	1	1	1	0	0	1	0	1	1	
+	0	0	0	1	0	1	0	0	0	1	
carry					1						1
	0	1	1	0	1	1	1	0	1	0	

Hence, the output of our PRG is $0,1,1,0,1,1,1,0,1,0,\ldots$

Important comment. Make sure you realize in which way this CSS PRG is much less predictable than a single LFSR! A single LFSR with ℓ registers is completely predictable since knowing ℓ bits of output (determines the state of the LFSR and) allows us to predict all future output. On the other hand, it is not so simple to deduce the state of the CSS PRG from the output. For instance, the initial (0, 1, ...) output could have been generated as (0, 0, ...) + (0, 1, ...) + (0, 0, ...) or (1, 0, ...) + (1, 0, ...) + (1, 1, ...).

[In this case, we actually don't learn anything about the registers of each individual LFSR. However, we do learn how their values have to match up. That's the correlation that is exploited in correlation attacks, like the one described last class for the actual CSS scheme.]

Example 72. (bonus challenge) Eventually the output in the previous example has to repeat (though it doesn't need to be perfectly periodic; see Example 69). Once it repeats, what is the period?

Note. The state of the system is determined by 3+4+1=8 bits (3 bits for LFSR-1, 4 bits for LFSR-2, and 1 bit for the carry). Hence, there are $2^8 = 256$ many states. Since state with everything 0 is again special, that means that after at most 255 steps, our PRG will reach a state it has been in before. At that point, everything will repeat.

CSS (and many other examples in recent history) teach us one important lesson:

Do not implement your own ideas for serious crypto!

We will soon see that there exist cryptosystems which are believed to be secure. While this is not proven in any case, we do know that certain of these are in fact secure (if implemented correctly) if and only if a certain important mathematical problem cannot be easily solved.

- So, to crack such a system, one has to solve a mathematical problem that many people care about deeply. If this happens, you will most likely read about it in the (academic) news, and you will have an opportunity to update your system in time (most likely, you'll hear about progress much earlier).
- On the other hand, if you use a cryptosystem that is not well-studied, then it may well happen that an adversary breaks your system and keeps exploiting the security leak without you ever learning about it.